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Surrogate-Based Modeling and Optimization Slawomir Koziel 2013-06-06

Contemporary engineering design is heavily based on computer simulations. Accurate, high-fidelity simulations are used not only for design verification but, even more importantly, to adjust parameters of the system to have it meet given performance requirements. Unfortunately, accurate simulations are often computationally very expensive with evaluation times as long as hours or even days per design, making design automation using conventional methods impractical. These and other problems can be alleviated by the development and employment of so-called surrogates that reliably represent the expensive, simulation-based model of the system or device of interest but they are much more reasonable and analytically tractable. This volume features surrogate-based modeling and optimization techniques, and their applications for solving difficult and computationally expensive engineering design problems. It begins by presenting the basic concepts and formulations of the surrogate-based modeling and optimization paradigm and then discusses relevant modeling techniques, optimization algorithms and design procedures, as well as state-of-the-art developments. The chapters are self-contained with basic concepts and formulations along with applications and examples. The book will be useful to researchers in engineering and mathematics, in particular those who employ computationally heavy simulations in their design work.

100 Power Tips for FPGA Designers

VHDL: Programming by Example Douglas Perry 2002-06-02 * Teaches VHDL by example * Includes tools for simulation and synthesis * CD-ROM containing Code/Design examples and a working demo of ModelSIM

Simulation and Optimization of Digital Circuits Vazgen Melikyan 2018-04-12 This book describes new, fuzzy logic-based mathematical apparatus, which enable readers to work with continuous variables, while implementing whole circuit simulations with speed, similar to gate-level simulators and accuracy, similar to circuit-level simulators. The author demonstrates newly developed principles of digital integrated circuit simulation and optimization that take into

consideration various external and internal destabilizing factors, influencing the operation of digital ICs. The discussion includes factors including radiation, ambient temperature, electromagnetic fields, and climatic conditions, as well as non-ideality of interconnects and power rails.

Digital System Design Dawoud Shenouda Dawoud 2010-04-10 Today, embedded systems are widely deployed in just about every piece of machinery from toasters to spacecrafts, and embedded system designers face many challenges. They are asked to produce increasingly complex systems using the latest technologies, but these technologies are changing faster than ever. They are asked to produce better quality designs with a shorter time-to-market. They are asked to implement increasingly complex functionality but, more importantly, to satisfy numerous other constraints. To achieve these current goals, the designer must be aware of such design constraints and, more importantly, the factors that have a direct effect on them. One of the challenges facing embedded system designers is the selection of the optimum processor for the application in hand: single-purpose, general-purpose, or application specific. Microcontrollers are one member of the family of the application specific processors. Digital System Design concentrates on the use of a microcontroller as the embedded system's processor and how to use it in many embedded system applications. The book covers both the hardware and software aspects needed to design using microcontrollers and is ideal for undergraduate students and engineers that are working in the field of digital system design.

Verilog HDL Samir Palnitkar 2003 VERILOG HDL, Second Edition by Samir Palnitkar With a Foreword by Prabhu Goel Written for both experienced and new users, this book gives you broad coverage of Verilog HDL. The book stresses the practical design and verification perspective of Verilog rather than emphasizing only the language aspects. The information presented is fully compliant with the IEEE 1364-2001 Verilog HDL standard. Among its many features, this edition-

- Describes state-of-the-art verification methodologies
- Provides full coverage of gate, dataflow (RTL), behavioral and switch modeling
- Introduces you to the Programming Language Interface (PLI)
- Describes logic synthesis methodologies
- Explains timing and delay simulation
- Discusses user-defined primitives
- Offers many practical modeling tips

Includes over 300 illustrations, examples, and exercises, and a Verilog resource list. Learning objectives and summaries are provided for each chapter. About the CD-ROM The CD-ROM contains a Verilog simulator with a graphical user interface and the source code for the examples in the book. What people are saying about Verilog HDL- "Mr. Palnitkar illustrates how and why Verilog HDL is used to develop today's most complex digital designs. This book is valuable to both the novice and the experienced Verilog user. I highly recommend it to anyone exploring Verilog based design." -Rajeev Madhavan, Chairman and CEO, Magma Design Automation "This book is unique in its breadth of information on Verilog and Verilog-related topics. It is fully compliant with the IEEE 1364-2001 standard, contains all the information that you need on the basics, and devotes several chapters to advanced topics such as verification, PLI, synthesis and modeling techniques." -Michael McNamara, Chair, IEEE 1364-2001 Verilog Standards

Organization This has been my favorite Verilog book since I picked it up in college. It is the only book that covers practical Verilog. A must have for beginners and experts." -Berend Ozceri, Design Engineer, Cisco Systems, Inc. "Simple, logical and well-organized material with plenty of illustrations, makes this an ideal textbook." -Arun K. Somani, Jerry R. Junkins Chair Professor, Department of Electrical and Computer Engineering, Iowa State University, Ames PRENTICE HALL Professional Technical Reference Upper Saddle River, NJ 07458 www.phptr.com ISBN: 0-13-044911-3

Modeling and Control of Hybrid Propulsion System for Ground Vehicles Yuan Zou 2018-07-02 This book focuses on the systematic design of architectures, parameters and control of typical hybrid propulsion systems for wheeled and tracked vehicles based on a combination of theoretical research and engineering practice. Adopting a mechatronic system dynamics perspective, principles and methods from the fields of optimal control and system optimization are applied in order to analyze the hybrid propulsion configuration and controller design. Case investigations for typical hybrid propulsion systems of wheeled and tracked ground vehicles are also provided.

Optoelectronics Sergei Pyshkin 2017-07-12 Optoelectronics - Advanced Device Structures (Book IV) is following the Optoelectronics (Books I, II, and III) published in 2011, 2013, and 2015, as part of the InTech collection of international works on optoelectronics. Accordingly, as with the first three books of the collection, this book covers recent achievements by specialists around the world. The growing number of countries participating in this endeavor as well as joint participation of the US and Moldova scientists in edition of this book testifies to the unifying effect of science. An interested reader will find in the book the description of properties and applications employing organic and inorganic materials, as well as the methods of fabrication and analysis of operation and regions of application of modern optoelectronic devices.

Computer Engineering and Technology Weixia Xu 2018-01-02 This book constitutes the refereed proceedings of the 21st CCF Conference on Computer Engineering and Technology, NCCET 2017, held in Xiamen, China, in August 2017. The 13 full papers presented were carefully reviewed and selected from 108 submissions. They address topics such as processor architecture; application specific processors; computer application and software optimization; technology on the horizon.

Rapid Prototyping of Digital Systems James O. Hamblen 2007-05-08 Rapid Prototyping of Digital Systems, Second Edition provides an exciting and challenging laboratory component for an undergraduate digital logic design class. The more advanced topics and exercises are also appropriate for consideration at schools that have an upper level course in digital logic or programmable logic. Design engineers working in industry will also want to consider this book for a rapid introduction to FPLD technology and logic synthesis using commercial CAD tools, especially if they have not had previous

experience with the new and rapidly evolving technology. Two tutorials on the Altera CAD tool environment, an overview of programmable logic, and a design library with several easy-to-use input and output functions were developed for this book to help the reader get started quickly. Early design examples use schematic capture and library components. VHDL is used for more complex designs after a short introduction to VHDL-based synthesis. A coupon is included with the text for purchase of the new UP 1X board. The additional logic and memory in the UP 1X's FLEX 10K70 is useful on larger design projects such as computers and video games. The second edition includes an update chapter on programmable logic, new robot sensors and projects, optional Verilog examples, and a meta assembler which can be used to develop assemble language programs for the computer designs in Chapters 8 and 13.

CERN. 2009

Silicon Photonics III Lorenzo Pavesi 2016-01-08 This book is volume III of a series of books on silicon photonics. It reports on the development of fully integrated systems where many different photonics component are integrated together to build complex circuits. This is the demonstration of the fully potentiality of silicon photonics. It contains a number of chapters written by engineers and scientists of the main companies, research centers and universities active in the field. It can be of use for all those persons interested to know the potentialities and the recent applications of silicon photonics both in microelectronics, telecommunication and consumer electronics market.

Applied Reconfigurable Computing Stephan Wong 2017-03-30 This book constitutes the refereed proceedings of the 13th International Symposium on Applied Reconfigurable Computing, ARC 2017, held in Delft, The Netherlands, in April 2017. The 17 full papers and 11 short papers presented in this volume were carefully reviewed and selected from 49 submissions. They are organized in topical sections on adaptive architectures, embedded computing and security, simulation and synthesis, design space exploration, fault tolerance, FPGA-based designs, neural networks, and languages and estimation techniques.

Proceedings of the Tenth Workshop on Electronics for LCH and Future Experiments 2004

Real-time Digital Signal Processing for Software-defined Optical Transmitters and Receivers Schmogrow, Rene Marcel 2014-11-21

Fundamentals of Digital Logic with Verilog Design Stephen Brown 2007-05-14 Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples. Use of CAD software is well integrated into the book. A CD-ROM that contains Altera's Quartus CAD software comes free with every copy of the text. The CAD software provides automatic mapping of a

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design written in Verilog into Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). Students will be able to try, firsthand, the book's Verilog examples (over 140) and homework problems. Engineers use Quartus CAD for designing, simulating, testing and implementing logic circuits. The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language. Students will be able to: enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices (using the school's laboratory facilities) Verilog is a complex language, so it is introduced gradually in the book. Each Verilog feature is presented as it becomes pertinent for the circuits being discussed. To teach the student to use the Quartus CAD, the book includes three tutorials.

Intelligent Communication Technologies and Virtual Mobile Networks S. Balaji 2019-08-12 This book presents the outcomes of the Intelligent Communication Technologies and Virtual Mobile Networks Conference (ICICV 2019) held in Tirunelveli, India, on February 14–15, 2019. It presents the state of the art in the field, identifying emerging research topics and communication technologies and defining the future of intelligent communication approaches and virtual computing. In light of the tremendous growth ICT, it examines the rapid developments in virtual reality in communication technology and high-quality services in mobile networks, including the integration of virtual mobile computing and communication technologies, which permits new technologies based on the resources and services of computational intelligence, big data analytics, Internet of Things (IoT), 5G technology, automation systems, sensor networks, augmented reality, data mining, and vehicular ad hoc networks with massive cloud-based backend. These services have a significant impact on all areas of daily life, like transportation, e-commerce, health care, secure communication, location detection, smart home, smart city, social networks and many more.

The IoT Physical Layer Ibrahim (Abe) M. Elfadel 2018-09-03 This book documents some of the most recent advances on the physical layer of the Internet of Things (IoT), including sensors, circuits, and systems. The application area selected for illustrating these advances is that of autonomous, wearable systems for real-time medical diagnosis. The book is unique in that it adopts a holistic view of such systems and includes not only the sensor and processing subsystems, but also the power, communication, and security subsystems. Particular attention is paid to the integration of these IoT subsystems as well as the prototyping platforms needed for achieving such integration. Other unique features include the discussion of energy-harvesting subsystems to achieve full energy autonomy and the consideration of hardware security as a requirement for the integrity of the IoT physical layer. One unifying thread of the various designs considered in this book is that they have all been fabricated and tested in an advanced, low-power CMOS process, namely GLOBALFOUNDRIES 65nm CMOS LPe.

Networking Communication and Data Knowledge Engineering Gregorio Martinez Perez 2017-11-13 Data science, data engineering and knowledge engineering requires networking and communication as a backbone and have wide scope of implementation in engineering sciences. Keeping this ideology in preference, this book includes the insights that reflect the advances in these fields from upcoming researchers and leading academicians across the globe. It contains high-quality peer-reviewed papers of 'International Conference on Recent Advancement in Computer, Communication and Computational Sciences (ICRACCCS 2016)', held at Janardan Rai Nagar Rajasthan Vidyapeeth University, Udaipur, India, during 25–26 November 2016. The volume covers variety of topics such as Advanced Communication Networks, Artificial Intelligence and Evolutionary Algorithms, Advanced Software Engineering and Cloud Computing, Image Processing and Computer Vision, and Security. The book will help the perspective readers from computer industry and academia to derive the advances of next generation communication and computational technology and shape them into real life applications.

SystemVerilog for Verification Chris Spear 2012-02-14 Based on the highly successful second edition, this extended edition of *SystemVerilog for Verification: A Guide to Learning the Testbench Language Features* teaches all verification features of the SystemVerilog language, providing hundreds of examples to clearly explain the concepts and basic fundamentals. It contains materials for both the full-time verification engineer and the student learning this valuable skill. In the third edition, authors Chris Spear and Greg Tumbush start with how to verify a design, and then use that context to demonstrate the language features, including the advantages and disadvantages of different styles, allowing readers to choose between alternatives. This textbook contains end-of-chapter exercises designed to enhance students' understanding of the material. Other features of this revision include: New sections on static variables, print specifiers, and DPI from the 2009 IEEE language standard Descriptions of UVM features such as factories, the test registry, and the configuration database Expanded code samples and explanations Numerous samples that have been tested on the major SystemVerilog simulators *SystemVerilog for Verification: A Guide to Learning the Testbench Language Features, Third Edition* is suitable for use in a one-semester SystemVerilog course on SystemVerilog at the undergraduate or graduate level. Many of the improvements to this new edition were compiled through feedback provided from hundreds of readers.

The Designer's Guide to Spice and Spectre® Ken Kundert 2006-04-11 Engineering productivity in integrated circuit product design and development today is limited largely by the effectiveness of the CAD tools used. For those domains of product design that are highly dependent on transistor-level circuit design and optimization, such as high-speed logic and memory, mixed-signal analog-digital interfaces, RF functions, power integrated circuits, and so forth, circuit simulation is perhaps the single most important tool. As the complexity and performance of integrated electronic systems has increased with scaling of technology feature size, the capabilities and sophistication of the underlying

circuit simulation tools have correspondingly increased. The absolute size of circuits requiring transistor-level simulation has increased dramatically, creating not only problems of computing power resources but also problems of task organization, complexity management, output representation, initial condition setup, and so forth. Also, as circuits of more complexity and mixed types of functionality are attacked with simulation, the spread between time constants or event time scales within the circuit has tended to become wider, requiring new strategies in simulators to deal with large time constant spreads.

WiMAX Modeling: Techniques and Applications Bhavin S. Sedani 2021-08-26 □ This book provides information about wireless systems and WiMAX modeling. The authors provide various techniques for the WiMAX systems such as antenna diversity and Alamouti coding. The performance of these systems is tested using various types of data and the results of systems are presented and discussed. Additional topics include WiMAX simulation using diversity techniques and real time WiMAX system modeling. The book pertains to researchers, academics, students, and professionals. Provides information about wireless system modeling and WiMAX systems; Presents WiMAX system modeling using antenna diversity techniques and the Alamouti coding scheme; Includes real time WiMAX system modeling for speech signal and digital images.

Built In Test for VLSI Paul H. Bardell 1987-10-20 This handbook provides ready access to all of the major concepts, techniques, problems, and solutions in the emerging field of pseudorandom pattern testing. Until now, the literature in this area has been widely scattered, and published work, written by professionals in several disciplines, has treated notation and mathematics in ways that vary from source to source. This book opens with a clear description of the shortcomings of conventional testing as applied to complex digital circuits, reviewing by comparison the principles of design for testability of more advanced digital technology. Offers in-depth discussions of test sequence generation and response data compression, including pseudorandom sequence generators; the mathematics of shift-register sequences and their potential for built-in testing. Also details random and memory testing and the problems of assessing the efficiency of such tests, and the limitations and practical concerns of built-in testing.

Fault Diagnosis of Analog Integrated Circuits Prithviraj Kabisatpathy 2006-01-13 Enables the reader to test an analog circuit that is implemented either in bipolar or MOS technology. Examines the testing and fault diagnosis of analog and analog part of mixed signal circuits. Covers the testing and fault diagnosis of both bipolar and Metal Oxide Semiconductor (MOS) circuits and introduces . Also contains problems that can be used as quiz or homework.

High-speed Serial Buses in Embedded Systems Feng Zhang 2020-01-03 This book describes the most frequently used high-speed serial buses in embedded systems, especially those used by FPGAs. These buses employ SerDes, JESD204, SRI0, PCIE, Aurora and SATA protocols for chip-to-chip and board-to-board communication,

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and PCIe, VPX, FC and Infiniband protocols for inter-chassis communication. For each type, the book provides the bus history and version info, while also assessing its advantages and limitations. Furthermore, it offers a detailed guide to implementing these buses in FPGA design, from the physical layer and link synchronization to the frame format and application command. Given its scope, the book offers a valuable resource for researchers, R&D engineers and graduate students in computer science or electronics who wish to learn the protocol principles, structures and applications of high-speed serial buses.

Proceedings of International Conference on Artificial Intelligence, Smart Grid and Smart City Applications L. Ashok Kumar 2020-03-12 Due to the complexity, and heterogeneity of the smart grid and the high volume of information to be processed, artificial intelligence techniques and computational intelligence appear to be some of the enabling technologies for its future development and success. The theme of the book is "Making pathway for the grid of future" with the emphasis on trends in Smart Grid, renewable interconnection issues, planning-operation-control and reliability of grid, real time monitoring and protection, market, distributed generation and power distribution issues, power electronics applications, computer-IT and signal processing applications, power apparatus, power engineering education and industry-institute collaboration. The primary objective of the book is to review the current state of the art of the most relevant artificial intelligence techniques applied to the different issues that arise in the smart grid development.

Analog Circuit Design Herman Casier 2008-03-19 Analog Circuit Design is based on the yearly Advances in Analog Circuit Design workshop. The aim of the workshop is to bring together designers of advanced analogue and RF circuits for the purpose of studying and discussing new possibilities and future developments in this field. Selected topics for AACD 2007 were: (1) Sensors, Actuators and Power Drivers for the Automotive and Industrial Environment; (2) Integrated PA's from Wireline to RF; (3) Very High Frequency Front Ends.

Microelectronics Education Adrian M. Ionescu 2013-03-19 In this book key contributions on developments and challenges in research and education on microelectronics, microsystems and related areas are published. Topics of interest include, but are not limited to: emerging fields in design and technology, new concepts in teaching, multimedia in microelectronics, industrial roadmaps and microelectronic education, curricula, nanoelectronics teaching, long distance education. The book is intended for academic education level and targets professors, researchers and PhDs involved in microelectronics and/or more generally, in electrical engineering, microsystems and material sciences. The 2004 edition of European Workshop on Microelectronics Education (EWME) is particularly focused on the interface between microelectronics and bio-medical sciences.

Digital Control of High-Frequency Switched-Mode Power Converters Luca Corradini 2015-07-10

Writing Testbenches: Functional Verification of HDL Models Janick Bergeron 2012-12-06 mental improvements during the same period. What is clearly needed in verification techniques and technology is the equivalent of a synthesis productivity breakthrough. In the second edition of *Writing Testbenches*, Bergeron raises the verification level of abstraction by introducing coverage-driven constrained-random transaction-level self-checking testbenches all made possible through the introduction of hardware verification languages (HVLs), such as e from Verisity and OpenVera from Synopsys. The state-of-art methodologies described in *Writing Test benches* will contribute greatly to the much-needed equivalent of a synthesis breakthrough in verification productivity. I not only highly recommend this book, but also I think it should be required reading by anyone involved in design and verification of today's ASIC, SoCs and systems. Harry Foster Chief Architect Verplex Systems, Inc. xviii *Writing Testbenches: Functional Verification of HDL Models* PREFACE If you survey hardware design groups, you will learn that between 60% and 80% of their effort is now dedicated to verification.

Recent Advances in Modeling and Simulation Tools for Communication Networks and Services Nejat Ince 2007-09-20 This book contains a selection of papers presented at a symposium organized under the aegis of COST Telecommunications Action 285. COST (European Cooperation in the field of Scientific and Technical Research) is a framework for scientific and technical cooperation, allowing the coordination of national research on a European level. Action 285 sought to enhance existing tools and develop new modeling and simulation tools.

High Speed Serdes Devices and Applications David Robert Stauffer 2008-12-19 The simplest method of transferring data through the inputs or outputs of a silicon chip is to directly connect each bit of the datapath from one chip to the next chip. Once upon a time this was an acceptable approach. However, one aspect (and perhaps the only aspect) of chip design which has not changed during the career of the authors is Moore's Law, which has dictated substantial increases in the number of circuits that can be manufactured on a chip. The pin densities of chip packaging technologies have not increased at the same pace as has silicon density, and this has led to a prevalence of High Speed Serdes (HSS) devices as an inherent part of almost any chip design. HSS devices are the dominant form of input/output for many (if not most) high-integration chips, moving serial data between chips at speeds up to 10 Gbps and beyond. Chip designers with a background in digital logic design tend to view HSS devices as simply complex digital input/output cells. This view ignores the complexity associated with serially moving billions of bits of data per second. At these data rates, the assumptions associated with digital signals break down and analog factors demand consideration. The chip designer who oversimplifies the problem does so at his or her own peril.

FPGA-Based Embedded System Developer's Guide A. Arockia Bazil Raj 2017-10-30 The book covers various aspects of VHDL programming and FPGA interfacing with

examples and sample codes giving an overview of VLSI technology, digital circuits design with VHDL, programming, components, functions and procedures, and arithmetic designs followed by coverage of the core of external I/O programming, algorithmic state machine based system design, and real-world interfacing examples.

Fundamentals and Standards in Hardware Description Languages Jean Mermet

2012-12-06 The second half of this century will remain as the era of proliferation of electronic computers. They did exist before, but they were mechanical. During next century they may perform other mutations to become optical or molecular or even biological. Actually, all these aspects are only fancy dresses put on mathematical machines. This was always recognized to be true in the domain of software, where "machine" or "high level" languages are more or less rigorous, but immaterial, variations of the universally accepted mathematical language aimed at specifying elementary operations, functions, algorithms and processes. But even a mathematical machine needs a physical support, and this is what hardware is all about. The invention of hardware description languages (HDL's) in the early 60's, was an attempt to stay longer at an abstract level in the design process and to push the stage of physical implementation up to the moment when no more technology independent decisions can be taken. It was also an answer to the continuous, exponential growth of complexity of systems to be designed. This problem is common to hardware and software and may explain why the syntax of hardware description languages has followed, with a reasonable delay of ten years, the evolution of the programming languages: at the end of the 60's they were "Algol like" , a decade later "Pascal like" and now they are "C or ADA-like". They have also integrated the new concepts of advanced software specification languages.

Domain Specific High-Level Synthesis for Cryptographic Workloads Ayesha Khalid

2019-03-28 This book offers an in-depth study of the design and challenges addressed by a high-level synthesis tool targeting a specific class of cryptographic kernels, i.e. symmetric key cryptography. With the aid of detailed case studies, it also discusses optimization strategies that cannot be automatically undertaken by CRYKET (Cryptographic kernels toolkit. The dynamic nature of cryptography, where newer cryptographic functions and attacks frequently surface, means that such a tool can help cryptographers expedite the very large scale integration (VLSI) design cycle by rapidly exploring various design alternatives before reaching an optimal design option. Features include flexibility in cryptographic processors to support emerging cryptanalytic schemes; area-efficient multinational designs supporting various cryptographic functions; and design scalability on modern graphics processing units (GPUs). These case studies serve as a guide to cryptographers exploring the design of efficient cryptographic implementations.

Intelligent Communication, Control and Devices Rajesh Singh 2018-05-21 The book focuses on the integration of intelligent communication systems, control systems, and devices related to all aspects of engineering and sciences. It contains high-quality research papers presented at the 2nd international

conference, ICICCD 2017, organized by the Department of Electronics, Instrumentation and Control Engineering of University of Petroleum and Energy Studies, Dehradun on 15 and 16 April, 2017. The volume broadly covers recent advances of intelligent communication, intelligent control and intelligent devices. The work presented in this book is original research work, findings and practical development experiences of researchers, academicians, scientists and industrial practitioners.

System Level Hardware/Software Co-Design Joris van den Hurk 2013-04-17
Hierarchical design methods were originally introduced for the design of digital ICs, and they appeared to provide for significant advances in design productivity, Time-to-Market, and first-time right design. These concepts have gained increasing importance in the semiconductor industry in recent years. In the course of time, the supportive quality of hierarchical methods and their advantages were confirmed. *System Level Hardware/Software Co-design: An Industrial Approach* demonstrates the applicability of hierarchical methods to hardware / software codesign, and mixed analogue / digital design following a similar approach. Hierarchical design methods provide for high levels of design support, both in a qualitative and a quantitative sense. In the qualitative sense, the presented methods support all phases in the product life cycle of electronic products, ranging from requirements analysis to application support. Hierarchical methods furthermore allow for efficient digital hardware design, hardware / software codesign, and mixed analogue / digital design, on the basis of commercially available formalisms and design tools. In the quantitative sense, hierarchical methods have prompted a substantial increase in design productivity. *System Level Hardware/Software Co-design: An Industrial Approach* reports on a six year study during which time the number of square millimeters of normalized complexity an individual designer contributed every week rose by more than a factor of five. Hierarchical methods therefore enabled designers to keep track of the ever increasing design complexity, while effectively reducing the number of design iterations in the form of redesigns. *System Level Hardware/Software Co-design: An Industrial Approach* is the first book to provide a comprehensive, coherent system design methodology that has been proven to increase productivity in industrial practice. The book will be of interest to all managers, designers and researchers working in the semiconductor industry.

The Designer's Guide to Verilog-AMS Ken Kundert 2006-04-11 The Verilog Hardware Description Language (Verilog-HDL) has long been the most popular language for describing complex digital hardware. It started life as a proprietary language but was donated by Cadence Design Systems to the design community to serve as the basis of an open standard. That standard was formalized in 1995 by the IEEE in standard 1364-1995. About that same time a group named Analog Verilog International formed with the intent of proposing extensions to Verilog to support analog and mixed-signal simulation. The first fruits of the labor of that group became available in 1996 when the language definition of Verilog-A was released. Verilog-A was not intended to work directly with Verilog-HDL. Rather it was a language with similar syntax and related semantics that was

intended to model analog systems and be compatible with SPICE-class circuit simulation engines. The first implementation of Verilog-A soon followed: a version from Cadence that ran on their Spectre circuit simulator. As more implementations of Verilog-A became available, the group defining the analog and mixed-signal extensions to Verilog continued their work, releasing the definition of Verilog-AMS in 2000. Verilog-AMS combines both Verilog-HDL and Verilog-A, and adds additional mixed-signal constructs, providing a hardware description language suitable for analog, digital, and mixed-signal systems. Again, Cadence was first to release an implementation of this new language, in a product named AMS Designer that combines their Verilog and Spectre simulation engines.

Computers, Software Engineering, and Digital Devices Richard C. Dorf 2018-10-03
In two editions spanning more than a decade, *The Electrical Engineering Handbook* stands as the definitive reference to the multidisciplinary field of electrical engineering. Our knowledge continues to grow, and so does the Handbook. For the third edition, it has expanded into a set of six books carefully focused on a specialized area or field of study. Each book represents a concise yet definitive collection of key concepts, models, and equations in its respective domain, thoughtfully gathered for convenient access. *Computers, Software Engineering, and Digital Devices* examines digital and logical devices, displays, testing, software, and computers, presenting the fundamental concepts needed to ensure a thorough understanding of each field. It treats the emerging fields of programmable logic, hardware description languages, and parallel computing in detail. Each article includes defining terms, references, and sources of further information. Encompassing the work of the world's foremost experts in their respective specialties, *Computers, Software Engineering, and Digital Devices* features the latest developments, the broadest scope of coverage, and new material on secure electronic commerce and parallel computing.