

Vlsi Design Viva Questions With Answers

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Digital VLSI Systems Design Seetharaman Ramachandran 2007-06-14 This book provides step-by-step guidance on how to design VLSI systems using Verilog. It shows the way to design systems that are device, vendor and technology independent. Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards. The reader is taken step by step through different designs, from implementing a single digital gate to a massive design consuming well over 100,000 gates. All the design codes developed in this book are Register Transfer Level (RTL) compliant and can be readily used or amended to suit new projects.

Computer Architecture Interview Questions You'll Most Likely Be Asked Vibrant Publishers 2011-09-06 *Computer Architecture Interview Questions You'll Most Likely Be Asked* is a perfect companion to stand ahead above the rest in today's competitive job market.

Verilog: Frequently Asked Questions Shivakumar S. Chonnad 2007-05-08 The Verilog Hardware Description Language was first introduced in 1984. Over the 20 year history of Verilog, every Verilog engineer has developed his own personal "bag of tricks" for coding with Verilog. These tricks enable modeling or verifying designs more easily and more accurately. Developing this bag of tricks is often based on years of trial and error. Through experience, engineers learn that one specific coding style works best in some circumstances, while in another situation, a different coding style is best. As with any high-level language, Verilog often provides engineers several ways to accomplish a specific task. Wouldn't it be wonderful if an engineer first learning Verilog could start with another engineer's bag of tricks, without having to go through years of trial and error to decide which style is best for which circumstance? That is where this book becomes an invaluable resource. The book presents dozens of Verilog tricks of the trade on how to best use the Verilog HDL for modeling designs at various level of abstraction, and for writing test benches to verify designs. The book not only shows the correct ways of using Verilog for different situations, it also presents alternate styles, and discusses the pros and cons of these styles.

VLSI Physical Design: From Graph Partitioning to Timing Closure Andrew B. Kahng 2011-01-27 Design and optimization of integrated circuits are essential to the creation of new semiconductor chips, and physical optimizations are becoming more prominent as a result of semiconductor scaling. Modern chip design has become so complex that it is largely performed by specialized software, which is frequently updated to address advances in semiconductor technologies and increased problem complexities. A user of such software needs a high-level understanding of the underlying mathematical

models and algorithms. On the other hand, a developer of such software must have a keen understanding of computer science aspects, including algorithmic performance bottlenecks and how various algorithms operate and interact. "VLSI Physical Design: From Graph Partitioning to Timing Closure" introduces and compares algorithms that are used during the physical design phase of integrated-circuit design, wherein a geometric chip layout is produced starting from an abstract circuit design. The emphasis is on essential and fundamental techniques, ranging from hypergraph partitioning and circuit placement to timing closure.

VLSI Design Esteban Tlelo-Cuautle 2012-01-20 This book provides some recent advances in design nanometer VLSI chips. The selected topics try to present some open problems and challenges with important topics ranging from design tools, new post-silicon devices, GPU-based parallel computing, emerging 3D integration, and antenna design. The book consists of two parts, with chapters such as: VLSI design for multi-sensor smart systems on a chip, Three-dimensional integrated circuits design for thousand-core processors, Parallel symbolic analysis of large analog circuits on GPU platforms, Algorithms for CAD tools VLSI design, A multilevel memetic algorithm for large SAT-encoded problems, etc.

Structural VLSI Analog Circuit Design - Principles, Problem Sets and Solution Hints Hongjiang Song

Interview Questions and Answers Richard McMunn 2012-01-01

Principles of VLSI RTL Design Sanjay Churiwala 2011-05-04 Since register transfer level (RTL) design is less about being a bright engineer, and more about knowing the downstream implications of your work, this book explains the impact of design decisions taken that may give rise later in the product lifecycle to issues related to testability, data synchronization across clock domains, synthesizability, power consumption, routability, etc., all which are a function of the way the RTL was originally written. Readers will benefit from a highly practical approach to the fundamentals of these topics, and will be given clear guidance regarding necessary safeguards to observe during RTL design.

Study of Engineering and Career J Vinay Kumar 2018-04-20 There are many ways to apply knowledge to achieve a successful career. Different people have used different ideologies get to the top. What are the characteristics that will help you achieve success? This book caters not only to students stepping into the engineering fields or the corporate world for the first time but also to those who are stuck in the wrong profession. The book highlights the importance of knowing your field of education, the importance of personality, finding the right opportunity in different fields of work, choosing the right first employer, and other important decisions related to your career. This book is an essential read for anyone who wants to enter the field of engineering. The volume includes a good number of illustrations with detailed notes.

VLSI Reference Circuits - Theory, Design, and Applications Hongjiang Song

Characterization and Modeling of Digital Circuits Rohit Sharma 2018-06-11 This book provides a comprehensive overview of characterization techniques and advanced modeling of VLSI circuits for modern and advanced process nodes for timing, power, noise and variation models. Intended audience includes research professionals, graduate students, circuit and PDK designers, characterization engineers, CAD developers, managers, mentors, and the merely curious. It is organized to serve as a compendium to a beginner, a ready reference to intermediate and source for an expert.

Cracking the Coding Interview Gayle Laakmann McDowell 2011 Now in the 5th edition, Cracking the

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Coding Interview gives you the interview preparation you need to get the top software developer jobs. This book provides: 150 Programming Interview Questions and Solutions: From binary trees to binary search, this list of 150 questions includes the most common and most useful questions in data structures, algorithms, and knowledge based questions. 5 Algorithm Approaches: Stop being blind-sided by tough algorithm questions, and learn these five approaches to tackle the trickiest problems. Behind the Scenes of the interview processes at Google, Amazon, Microsoft, Facebook, Yahoo, and Apple: Learn what really goes on during your interview day and how decisions get made. Ten Mistakes Candidates Make -- And How to Avoid Them: Don't lose your dream job by making these common mistakes. Learn what many candidates do wrong, and how to avoid these issues. Steps to Prepare for Behavioral and Technical Questions: Stop meandering through an endless set of questions, while missing some of the most important preparation techniques. Follow these steps to more thoroughly prepare in less time.

Gateway to VLSI: Want to be an FPGA Engineer? Bharat Agarwal 2019-09-23 If you can spare half an hour, then we can guarantee success at your next VLSI (Very Large Scale Integration)-FPGA (Field Programmable Gate Array)-STA (Static Timing analysis) interview. Do you want to secure at least 3 to 4 job offers by succeeding at all the phone and on-site job interviews for the FPGA DESIGN ENGINEER position? Or do you simply want answers for the most frequently asked interview questions in VLSI-FPGA digital circuit design? Did you know that people who target question-answer type preparation for a job interview are 3-4 times more likely to get a job offer than those who don't? Did you also know that there is a set of questions that is likely to be repeatedly asked by interviewers across the industry, no matter who you talk with in the VLSI-FPGA digital design? After a total of 17 unsuccessful interviews, we thought of writing a book to help upcoming undergrads and experience professionals to get selected in such interviews. The book covers every dimension related to FPGA, Verilog, STA and Protocols. In simple words, don't search anything on the internet, this book is the Google of FPGA and Verilog.

Computer Aids for VLSI Design Steven M. Rubin 2009 This textbook, originally published in 1987, broadly examines the software required to design electronic circuitry, including integrated circuits. Topics include synthesis and analysis tools, graphics and user interface, memory representation, and more. The book also describes a real system called "Electric."

VLSI Interview Questions with Answers Sam Sony 2012 "You get very carefully chosen 83 of the most important, most likely to be asked questions with illustrated answered, when it comes to interviewing in the field of digital VLSI and ASIC design"--Amazon.com.

Principles of VLSI Design - Symmetry, Structures and Methods Hongjiang Song 2018-06-14 This book covers structural VLSI analog circuit design techniques for analog circuit blocks, such as operational amplifiers, PLL/DLL, bandgap reference, A/D D/A converters. Symmetry principles, structures, and methods are used to effectively mitigate VLSI PVT and other variations for better performance, functionality, and design productivity.

VLSI High-Speed I/O Circuits - Problems, Projects, and Questions Hongjiang Song 2014-03-11 This book is based on a collection of homework problems, design projects and sample interview questions for the VLSI High-Speed I/O Circuits class (EEE598) the author offered in the School of Engineering at Arizona State University. The materials cover various aspects of the design, analysis and application of VLSI high-speed I/O circuits. This book is intended to be used together with the VLSI High-Speed I/O Circuits textbook by the same author. It can also be used alone for the experienced readers.

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Publishers 2016-12-12 200 Data Structures & Algorithms Interview Questions 77 HR Interview Questions Real life scenario based questions Strategies to respond to interview questions 2 Aptitude Tests Data Structures & Algorithms Interview Questions You'll Most Likely Be Asked is a perfect companion to stand ahead above the rest in today's competitive job market. Rather than going through comprehensive, textbook-sized reference guides, this book includes only the information required immediately for job search to build an IT career. This book puts the interviewee in the driver's seat and helps them steer their way to impress the interviewer. The following is included in this book: a) 200 Data Structures & Algorithms Interview Questions, Answers and proven strategies for getting hired as an IT professional b) Dozens of examples to respond to interview questions c) 77 HR Questions with Answers and proven strategies to give specific, impressive, answers that help nail the interviews d) 2 Aptitude Tests download available on <https://www.vibrantpublishers.com>

IC Mask Design Christopher Saint 2002-06-14 Integrated Circuit Mask Design teaches integrated circuit (IC) processes, mask design techniques, and fundamental device concepts in everyday language. It develops ideas from the ground up, building complex concepts out of simple ones, constantly reinforcing what has been taught with examples, self-tests and sidebars covering the motivation behind the material covered.

A Collection of System Design Interview Questions Antonio Gulli 2016-07-16 A collection of System Design Interview Questions

VLSI Interview Questions with Answers Sam Sony 2012 If you can spare half an hour, then this ebook guarantees job search success with VLSI interview questions. Now you can ace all your interviews as you will access to the answers to the questions, which are most likely to be asked during VLSI interviews. You can do this completely risk free, as this book comes with 100% money back guarantee. To find out more details including what type of other questions book contains, please click on the BUY link.

VLSI Design Techniques for Analog and Digital Circuits Randall L. Geiger 1990

VLSI Noise Processing Circuits - Theoretical Bases and Implementations Hongjiang Song

Advanced VLSI Technology Cherry Bhargava 2022-09-01 The trend in design and manufacturing of very large-scale integrated (VLSI) circuits is towards smaller devices on increasing wafer dimensions. VLSI is the inter-disciplinary science of the process of creating an integrated circuit (IC) by combining thousands of transistors into a single chip. VLSI design can reduce the area of the circuit, making it less expensive and requiring less power. The book gives an understanding of the underlying principles of the subject. It not only focuses on circuit design process obeying VLSI rules but also on technological aspects of prototyping and fabrication. All the clocking processes, interconnects, and circuits of CMOS are explained in this book in an understandable format. The book provides contents on VLSI Physical Design Automation, Design of VLSI Devices and also its Impact on Physical Design. The book is intended as a reference book for senior undergraduate, first-year post graduate students, researchers as well as academicians in VLSI design, electronics & electrical engineering, and materials science. The basics and applications of VLSI design from STA, PDA and VLSI Testing along with FPGA based Prototyping are covered in a comprehensive manner. The latest technology used in VLSI design is discussed along with the available tools for FPGA prototyping as well as ASIC design. Each unit contains technical questions with solutions at the end. Technical topics discussed in the book include: • Static Timing Analysis • CMOS Layout and Design rules • Physical Design Automation • Testing of VLSI Circuits • Software tools for Frontend and Backend design.

Digital Vlsi Design Singh Ajay Kumar 2010-11-01

VLSI Modulation Circuits - Signal Processing, Data Conversion, and Power Management Hongjiang Song 2016-01-26 This is a textbook developed for a VLSI circuit design course series (EEE598) that the author has been offering in the Schools of Engineering at Arizona State University. The materials are organized into eighteen special topics covering the principles, the circuit design techniques and the applications of VLSI modulation in signal processing, data conversion, power amplification and power management.

Digital Logic Design Brian Holdsworth 2002-11-01 New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. *A highly accessible, comprehensive and fully up to date digital systems text *A well known and respected text now revamped for current courses *Part of the Newnes suite of texts for HND/1st year modules

The Art of Timing Closure Khosrow Golshan 2020-08-03 The Art of Timing Closure is written using a hands-on approach to describe advanced concepts and techniques using Multi-Mode Multi-Corner (MMMC) for an advanced ASIC design implementation. It focuses on the physical design, Static Timing Analysis (STA), formal and physical verification. The scripts in this book are based on Cadence® Encounter System™. However, if the reader uses a different EDA tool, that tool's commands are similar to those shown in this book. The topics covered are as follows: Data Structures Multi-Mode Multi-Corner Analysis Design Constraints Floorplan and Timing Placement and Timing Clock Tree Synthesis Final Route and Timing Design Signoff Rather than go into great technical depth, the author emphasizes short, clear descriptions which are implemented by references to authoritative manuscripts. It is the goal of this book to capture the essence of physical design and timing analysis at each stage of the physical design, and to show the reader that physical design and timing analysis engineering should be viewed as a single area of expertise. This book is intended for anyone who is involved in ASIC design implementation -- starting from physical design to final design signoff. Target audiences for this book are practicing ASIC design implementation engineers and students undertaking advanced courses in ASIC design.

Low-Power Design and Power-Aware Verification Progyna Khondkar 2017-10-05 Until now, there has been a lack of a complete knowledge base to fully comprehend Low power (LP) design and power aware (PA) verification techniques and methodologies and deploy them all together in a real design verification and implementation project. This book is a first approach to establishing a comprehensive PA knowledge base. LP design, PA verification, and Unified Power Format (UPF) or IEEE-1801 power format standards are no longer special features. These technologies and methodologies are now part of industry-standard design, verification, and implementation flows (DVIF). Almost every chip design today incorporates some kind of low power technique either through power management on chip, by dividing the design into different voltage areas and controlling the voltages, through PA dynamic and PA static verification, or their combination. The entire LP design and PA verification process involves thousands of techniques, tools, and methodologies, employed from the register transfer level (RTL) of design abstraction down to the synthesis or place-and-route levels of physical design. These techniques, tools, and methodologies are evolving everyday through the progression of design-verification complexity and more intelligent ways of handling that complexity by engineers, researchers, and corporate engineering policy makers.

CMOS VLSI Design: A Circuits and Systems Perspective Neil H. E. Weste 2011

CMOS Digital Integrated Circuits Sung-Mo Kang 2002 The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Static Timing Analysis Interview Questions with Answers Sam Sony 2012 If you can spare half an hour, then this ebook guarantees job search success with STA interview questions. Now you can ace all your interviews as you will access to the answers to the questions, which are most likely to be asked during VLSI interviews. You can do this completely risk free, as this book comes with 100% money back guarantee. To find out more details including what type of other questions book contains, please click on the BUY link.

Basic VLSI Design Technology Cherry Bhargava 2022-09-01 The current cutting-edge VLSI circuit design technologies provide end-users with many applications, increased processing power and improved cost effectiveness. This trend is accelerating, with significant implications on future VLSI and systems design. VLSI design engineers are always in demand for front-end and back-end design applications. The book aims to give future and current VLSI design engineers a robust understanding of the underlying principles of the subject. It not only focuses on circuit design processes obeying VLSI rules but also on technological aspects of fabrication. The Hardware Description Language (HDL) Verilog is explained along with its modelling style. The book also covers CMOS design from the digital systems level to the circuit level. The book clearly explains fundamental principles and is a guide to good design practices. The book is intended as a reference book for senior undergraduate, first-year post graduate students, researchers as well as academicians in VLSI design, electronics & electrical engineering and materials science. The basics and applications of VLSI design from digital system design to IC fabrication and FPGA Prototyping are each covered in a comprehensive manner. At the end of each unit is a section with technical questions including solutions which will serve as an excellent teaching aid to all readers. Technical topics discussed in the book include: • Digital System Design • Design flow for IC fabrication and FPGA based prototyping • Verilog HDL • IC Fabrication Technology • CMOS VLSI Design • Miscellaneous (It covers basics of Electronics, and Reconfigurable computing, PLDs, Latest technology etc.).

Verilog HDL Samir Palnitkar 2003 VERILOG HDL, Second Edition by Samir Palnitkar With a Foreword by Prabhu Goel Written for both experienced and new users, this book gives you broad coverage of Verilog HDL. The book stresses the practical design and verification perspective of Verilog rather than emphasizing only the language aspects. The information presented is fully compliant with the IEEE 1364-2001 Verilog HDL standard. Among its many features, this edition-
• Describes state-of-the-art verification methodologies
• Provides full coverage of gate, dataflow (RTL), behavioral and switch modeling
• Introduces you to the Programming Language Interface (PLI)
• Describes logic synthesis methodologies
• Explains timing and delay simulation
• Discusses user-defined primitives
• Offers many practical modeling tips
Includes over 300 illustrations, examples, and exercises, and a Verilog resource list. Learning objectives and summaries are provided for each chapter. About the CD-ROM The CD-ROM contains a Verilog simulator with a graphical user interface and the source code for the examples

in the book. What people are saying about Verilog HDL- "Mr. Palnitkar illustrates how and why Verilog HDL is used to develop today's most complex digital designs. This book is valuable to both the novice and the experienced Verilog user. I highly recommend it to anyone exploring Verilog-based design." - Rajeev Madhavan, Chairman and CEO, Magma Design Automation "This book is unique in its breadth of information on Verilog and Verilog-related topics. It is fully compliant with the IEEE 1364-2001 standard, contains all the information that you need on the basics, and devotes several chapters to advanced topics such as verification, PLI, synthesis and modeling techniques." - Michael McNamara, Chair, IEEE 1364-2001 Verilog Standards Organization "This has been my favorite Verilog book since I picked it up in college. It is the only book that covers practical Verilog. A must have for beginners and experts." - Berend Ozceri, Design Engineer, Cisco Systems, Inc. "Simple, logical and well-organized material with plenty of illustrations, makes this an ideal textbook." - Arun K. Somani, Jerry R. Junkins Chair Professor, Department of Electrical and Computer Engineering, Iowa State University, Ames PRENTICE HALL Professional Technical Reference Upper Saddle River, NJ 07458 www.phptr.com ISBN: 0-13-044911-3

Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits M. Bushnell 2006-04-11
The modern electronic testing has a forty year history. Test professionals hold some fairly large conferences and numerous workshops, have a journal, and there are over one hundred books on testing. Still, a full course on testing is offered only at a few universities, mostly by professors who have a research interest in this area. Apparently, most professors would not have taken a course on electronic testing when they were students. Other than the computer engineering curriculum being too crowded, the major reason cited for the absence of a course on electronic testing is the lack of a suitable textbook. For VLSI the foundation was provided by semiconductor device technology, circuit design, and electronic testing. In a computer engineering curriculum, therefore, it is necessary that foundations should be taught before applications. The field of VLSI has expanded to systems-on-a-chip, which include digital, memory, and mixed-signal subsystems. To our knowledge this is the first textbook to cover all three types of electronic circuits. We have written this textbook for an undergraduate "foundations" course on electronic testing. Obviously, it is too voluminous for a one-semester course and a teacher will have to select from the topics. We did not restrict such freedom because the selection may depend upon the individual expertise and interests. Besides, there is merit in having a larger book that will retain its usefulness for the owner even after the completion of the course. With equal tenacity, we address the needs of three other groups of readers.

Elements of Programming Interviews Adnan Aziz 2012-10-11
The core of EPI is a collection of over 300 problems with detailed solutions, including 100 figures, 250 tested programs, and 150 variants. The problems are representative of questions asked at the leading software companies. The book begins with a summary of the nontechnical aspects of interviewing, such as common mistakes, strategies for a great interview, perspectives from the other side of the table, tips on negotiating the best offer, and a guide to the best ways to use EPI. The technical core of EPI is a sequence of chapters on basic and advanced data structures, searching, sorting, broad algorithmic principles, concurrency, and system design. Each chapter consists of a brief review, followed by a broad and thought-provoking series of problems. We include a summary of data structure, algorithm, and problem solving patterns.

Nature-Inspired VLSI Circuits - From Concept to Implementation Hongjiang Song

Constraining Designs for Synthesis and Timing Analysis Sridhar Gangadharan 2014-07-08
This book serves as a hands-on guide to timing constraints in integrated circuit design. Readers will learn to maximize performance of their IC designs, by specifying timing requirements correctly. Coverage includes key aspects of the design flow impacted by timing constraints, including synthesis, static timing

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analysis and placement and routing. Concepts needed for specifying timing requirements are explained in detail and then applied to specific stages in the design flow, all within the context of Synopsys Design Constraints (SDC), the industry-leading format for specifying constraints.

Cracking Digital VLSI Verification Interview Robin Garg 2016-03-13 How should I prepare for a Digital VLSI Verification Interview? What all topics do I need to know before I turn up for an interview? What all concepts do I need to brush up? What all resources do I have at my disposal for preparation? What does an Interviewer expect in an Interview? These are few questions almost all individuals ponder upon before an interview. If you have these questions in your mind, your search ends here as keeping these questions in their minds, authors have written this book that will act as a golden reference for candidates preparing for Digital VLSI Verification Interviews. Aim of this book is to enable the readers practice and grasp important concepts that are applicable to Digital VLSI Verification domain (and Interviews) through Question and Answer approach. To achieve this aim, authors have not restricted themselves just to the answer. While answering the questions in this book, authors have taken utmost care to explain underlying fundamentals and concepts. This book consists of 500+ questions covering wide range of topics that test fundamental concepts through problem statements (a common interview practice which the authors have seen over last several years). These questions and problem statements are spread across nine chapters and each chapter consists of questions to help readers brush-up, test, and hone fundamental concepts that form basis of Digital VLSI Verification. The scope of this book however, goes beyond technical concepts. Behavioral skills also form a critical part of working culture of any company. Hence, this book consists of a section that lists down behavioral interview questions as well. Topics covered in this book:1. Digital Logic Design (Number Systems, Gates, Combinational, Sequential Circuits, State Machines, and other Design problems)2. Computer Architecture (Processor Architecture, Caches, Memory Systems)3. Programming (Basics, OOP, UNIX/Linux, C/C++, Perl)4. Hardware Description Languages (Verilog, SystemVerilog)5. Fundamentals of Verification (Verification Basics, Strategies, and Thinking problems)6. Verification Methodologies (UVM, Formal, Power, Clocking, Coverage, Assertions)7. Version Control Systems (CVS, GIT, SVN)8. Logical Reasoning/Puzzles (Related to Digital Logic, General Reasoning, Lateral Thinking)9. Non Technical and Behavioral Questions (Most commonly asked)In addition to technical and behavioral part, this book touches upon a typical interview process and gives a glimpse of latest interview trends. It also lists some general tips and Best-Known-Methods to enable the readers follow correct preparation approach from day-1 of their preparations. Knowing what an Interviewer looks for in an interviewee is always an icing on the cake as it helps a person prepare accordingly. Hence, authors of this book spoke to few leaders in the semiconductor industry and asked their personal views on "What do they look for while Interviewing candidates and how do they usually arrive at a decision if a candidate should be hired?". These leaders have been working in the industry from many-many years now and they have interviewed lots of candidates over past several years. Hear directly from these leaders as to what they look for in candidates before hiring them. Enjoy reading this book. Authors are open to your feedback. Please do provide your valuable comments, ratings, and reviews.

[Gateway to VLSI](#) Bharat Agarwal, Kshitij Goel 2019-10-04 If you can spare half an hour, then we can guarantee success at your next VLSI (Very Large Scale Integration)-FPGA (Field Programmable Gate Array)-STA (Static Timing analysis) interview. Do you want to secure at least 3 to 4 job offers by succeeding at all the phone and on-site job interviews for the FPGA DESIGN ENGINEER position? Or do you simply want answers for the most frequently asked interview questions in VLSI-FPGA digital circuit design? Did you know that people who target question-answer type preparation for a job interview are 3-4 times more likely to get a job offer than those who don't? Did you also know that there is a set of questions that is likely to be repeatedly asked by interviewers across the industry, no matter who you

talk with in the VLSI-FPGA digital design? After a total of 17 unsuccessful interviews, we thought of writing a book to help upcoming undergrads and experience professionals to get selected in such interviews. The book covers every dimension related to FPGA, Verilog, STA and Protocols. In simple words, don't search anything on the internet, this book is the Google of FPGA and Verilog.